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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/830,036	04/23/2004	Yoshihisa Kaminaga	252117US2SCONT	7508
22850	7590	08/09/2005	EXAMINER	
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. 1940 DUKE STREET ALEXANDRIA, VA 22314				VU, JIMMY T
ART UNIT		PAPER NUMBER		
		2821		

DATE MAILED: 08/09/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/830,036	KAMINAGA ET AL.
	Examiner	Art Unit
	Jimmy T. Vu	2821

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 23 April 2004.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-10 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1-10 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Irikura (U.S. Patent number 6,356,021 B2).

Regarding claim 1, Irikura discloses an electron gun assembly resistor comprising:
an insulating substrate (21) (Fig. 4);
a plurality of electrode elements (22A-22E) formed on the insulating substrate and looking like islands (Fig. 4);
a resistor element (23) connecting the electrode elements together and providing a predetermined resistance value; and
a plurality of metallic terminals (31A-31E) which include flanges in contact with the electrode elements, and which are connected to the electrode elements,
the electron gun assembly resistor satisfying $L_1 \leq L_2$,
where L_1 is an outer dimension of at least one of the electrode elements, and L_2 is an outer dimension of the flange of the metallic terminal that is connected to the electrode element whose outer dimension is L_1 (Figs. 1-7, col. 5, lines 1-50, col. 6, lines 35-60).

Regarding claims 2 and 5, Irikura discloses an electron gun assembly resistor wherein the flanges are located outward of outer peripheries of the electrode elements (Figs. 1-7).

Regarding claims 3 and 6, Irikura discloses an electron gun assembly resistor wherein the flanges include tip ends that are curved to cover the electrode elements (Figs. 1-7).

Regarding claim 4 Irikura discloses a comprising:

a face panel (102) (Fig. 7);
a funnel (103) (Fig. 7) integrally connected to the face panel;
a phosphor screen (105) (Fig. 7) formed on an inner surface of the face panel;
an electron gun assembly (108) arranged in a neck of the funnel, configured to emit electron beams toward the phosphor screen, and including a plurality of grid electrodes; and
an electron gun assembly resistor (Figs. 1-7) arranged in the neck (104) and juxtaposed to the electron gun assembly, the electron gun assembly resistor dividing a voltage based on a predetermined voltage division ratio and permitting a divided voltage to be applied to at least one of the grid electrodes,

the electrode gun assembly resistor comprising:

an insulating substrate (21) ;
a plurality of electrode elements (22A-22E) formed on the insulating substrate and looking like islands;
a resistor element (23) connecting the electrode elements together and providing a predetermined resistance value; and

a plurality of metallic terminals (31A-31E) which include flanges in contact with the electrode elements, and which are connected to the electrode elements, the electron gun assembly resistor satisfying $L_1 \leq L_2$,

where L_1 is an outer dimension of at least one of the electrode elements, and L_2 is an outer dimension of the flange of the metallic terminal that is connected to the electrode element whose outer dimension is L_1 (Figs. 1-7, col. 5, lines 1-50, col. 6, lines 35-60, col. 7, lines 5-25).

Regarding claim 7, Irikura discloses an electron gun assembly resistor configured to divide a voltage based on a predetermined voltage division ratio and to permit a divided voltage to be applied to an electrode of an electron gun assembly, the electron gun assembly resistor comprising:

an insulating substrate (21);
a plurality of electrode elements (22A-22E) formed on the insulating substrate;
a resistor element (23) connecting the electrode elements together and providing a predetermined resistance value;

an insulating coating layer (24a) which covers the resistor element; and
a plurality of metallic terminals (31A-31E) connected to the electrode elements, respectively,

the metallic terminals being arranged without exposing the electrode elements,
the insulating coating layer being coated on peripheries of the metallic terminals and being located away from the electrode elements (Figs. 1-7, col. 5, lines 1-50, col. 6, lines 35-60, col. 7, lines 5-25).

Regarding claim 8, Irikura discloses an electron gun assembly resistor wherein regions where the insulating coating layer covers the peripheries of the electrode elements are regions where the insulating substrate has surface portions that are electrically charged to have a potential higher than that of the metallic terminals (Figs. 1-7).

Regarding claim 9, Irikura discloses an electron gun assembly resistor wherein the metallic terminals include flanges which are in contact with the electrode elements, and the flanges have an outer dimension greater than that of the electrode elements and include portions located outward of the peripheries of the electrode elements (Figs. 1-7).

Regarding claim 10, Irikura discloses an electron gun assembly resistor wherein the insulating coating layer covers the peripheries of the flanges of the metallic terminals without exposing the insulating substrate (Figs. 1-7).

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jimmy T Vu whose telephone number is (571) 272-1832. The examiner can normally be reached on M - F: 9 - 6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong can be reached on (571) 272-1834. The fax phone numbers for the organization where this application or proceeding is assigned are (571) 273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (571) 272-2800.

Jimmy Vu

July 29, 2005



TUYET VO
PRIMARY EXAMINER